ABC-801.PDF
Slot B
R1
Resistive Connection to Address 14
C2
C1
ABC-801.AI8
B
A
+12V
Test Clock
GND
Test Data Output
+5V
Interrupt B
Interrupt D
PRSNT1#
RESERVED
PRSNT2#
RESERVED
GND
Clock
GND
Request
+V I/O
Address 31
Address 29
GND
Address 27
C/BE 3
+3.3V
Address 25
Request
GND
Address 23
Address 21
GND
Address 19
+3.3V
C/BE 2
Address 17
GND
Initiator Ready
+3.3V
Device Select
GND
Lock
Parity Error
3.3V
System Error
+3.3V
C/BE 1
Address 14
M66EN/GND
Address 12
Address 10
GND
Address 8
Address 7
3.3V
Address 5
Address 3
GND
Address 1
+V I/O
Acknowledge 64-bit
+5V
+5V
Test Reset
+12V
Test Mode Select
Test Mode Input
+5V
Interrupt A
Interrupt C
+5V
Reserved
+V I/O
Reserved
Reserved
Reset
+V I/O
Grant
GND
Reserved
Address 30
Address 28
GND
Address 26
Address 24
Init Device Select
+3.3V
Address 22
GND
Address 20
+3.3V
Address 18
Address 16
+3.3V
Cycle Frame
GND
Target Ready
+3.3V
Stop
Snoop Dond
Snoop Backoff
GND
PAR
Address 15
+3.3V
Address 13
Address 11
GND
Address 9
C/BE 0
+3.3V
Address 6
Address 4
GND
Address 2
Address 0
GND
+V I/O
+5V
+5V
+5V
A9
B14
A14
Reserved Pins in PCI Spec. traces present on 6x00 Riser