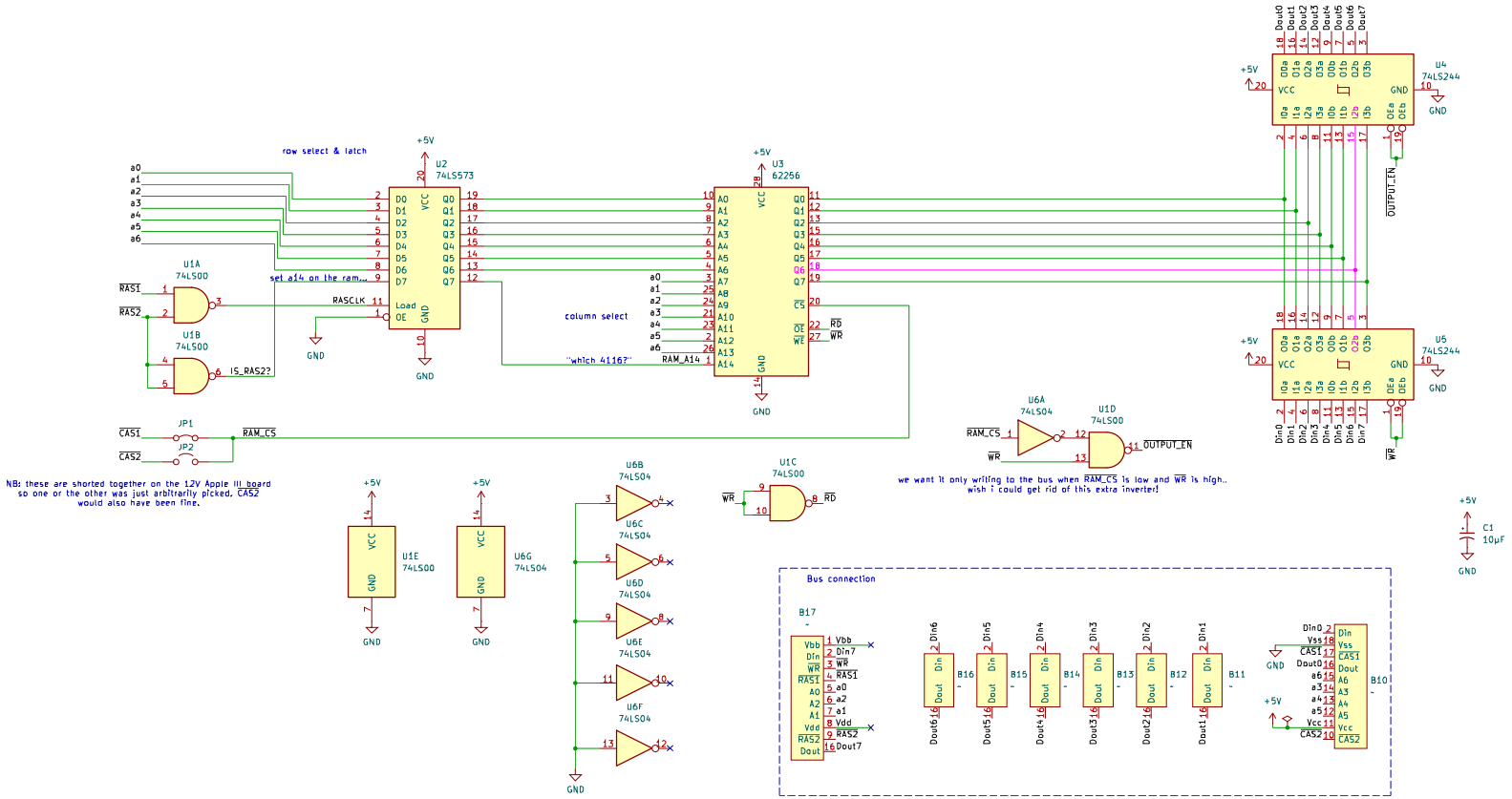
row select & latch
U2
74LS573
U3
62256
U4
74LS244
U5
74LS244
U1A
74LS00
U1B
74LS00
U1C
74LS00
U1D
74LS00
U1E
74LS00
U6A
74LS04
U6B
74LS04
U6C
74LS04
U6D
74LS04
U6E
74LS04
U6F
74LS04
U6G
74LS04
JP1
JP2
RASCLK
IS_RAS2?
column select
"which 4116?"
RAM_A14
RAM_CS
OUTPUT_EN
we want it only writing to the bus when RAM_CS is low and WR is high..
wish I could get rid of this extra inverter!
NB: these are shorted together on the 12V Apple III board
so one or the other was just arbitrarily picked. CAS2
would also have been fine.
Bus connection
B17
B10
B11
B12
B13
B14
B15
B16
C1
10µF
Sheet: /
File: apple-iii-32kbyte-module.kicad_sch
Title:
Size: A3
Date:
Rev:
KiCad E.D.A. 9.0.4
id: 1/1